

**Notice of References Cited**Application/Control No.  
10/059,558Applicant(s)/Patent Under  
Reexamination  
HOFMANN ET AL.Examiner  
Gopal C. RayArt Unit  
2111

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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	B	US-5,796,413	08-1998	Shipp et al.	345/522
	C	US-5,923,859	07-1999	Melo et al.	710/113
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	L	US-			
	M	US-			

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**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	"VLSI design of a bus arbitration module for the 68000 series of microprocessors" by Ososanya, E.T.; McGlone, M.D.: Strong, T.D. (abstract only)
	V	"An efficient controller scheme for MPEG-2 video decoder" by Nam Ling; Nien-Tsu Wang; Duan-Juat Ho (abstract only)
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.